EE/CprE/SE 492 Status REPORT 04

2/28/25 – 3/13/25

Group number: sdmay25-31

Project title: Implement Curriculum Around the i281 Processor

Client & Advisor: Prof. Alexander Stoytchev

Team Members:

Ethan Uhrich - Team Lead & Treasurer
Ariana Dirksen - Editor & Note Taker
Tessa Morgan - Task Manager & Webmaster
Gigi Harrabi - Client Interaction & Outreach Coordinator

Milestones Reached

In this period we finished the rough draft of Lab 5, started working on Lab 10 and continued working on Lab 8 and Lab 9. We also created the next drafts for Lab 1 and Lab 2 by updating them in respect to requests made by our client. Arranged for participation in an outreach event in April.

Accomplishments Over the Period

Ethan Uhrich: Finished Lab 5, Ordered parts for lab 9.
Ariana Dirksen: Helped finish the rough draft of Lab 5, edited Lab 1 and began work on Lab 10 rough draft.
Tessa Morgan: Second draft for lab 2 and continued work on EEPROM lab.
Gigi Harrabi: Lab 9 rough draft including pre lab and EEPROM debugging.

Individual Contributions

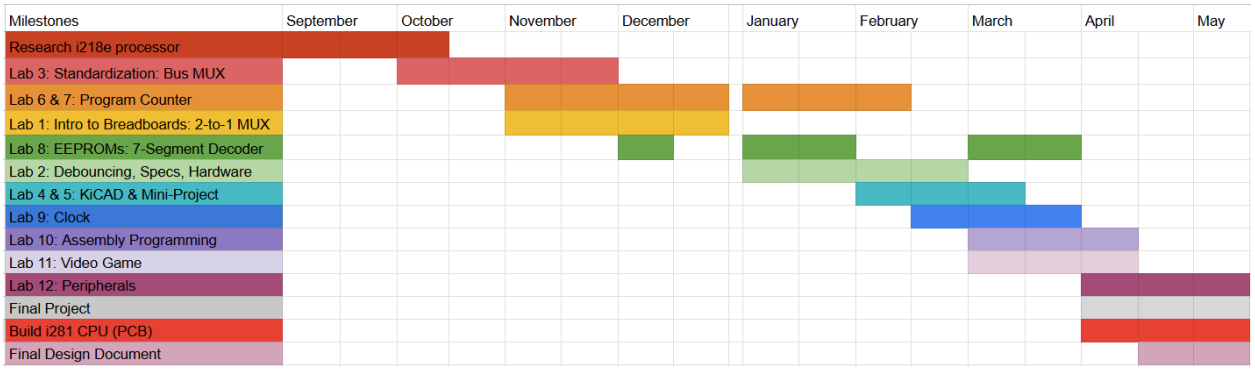
NAME	Individual Contributions	Hours this week	HOURS cumulative
Ethan Uhrich	Finished Lab 5, Ordered parts for lab 9	8	89
Ariana Dirksen	Helped finish the rough draft of Lab 5, edited Lab 1 and began work on Lab 10 rough draft.	13	123
Tessa Morgan	Second draft for lab 2 and continued work on EEPROM lab (Lab 8).	14	118
Gigi Harrabi	Lab 9 rough draft including pre lab and EEPROM debugging.	12	101

Plans for the Upcoming Week

Ethan Uhrich: Work on rough draft for lab 10. Make edits to lab 5 when they are ready. Finish lab 5 video.
Ariana Dirksen: Finish the rough draft for Lab 10. Continue making edits to Labs based on feedback received from the client. Catch up on Lab answer sheets for Lab 4 + 5.
Tessa Morgan: Continue figuring out EEPROM lab (Lab 8).
Gigi Harrabi: Working on EEPROM (Lab 8) with Tessa and also building the clock divider (Lab 9).

Project Work

Updated Gantt Chart for adjusted priorities



Research

Looking into the format for loading assembly programs on the simulator. As well as relevant information for an assembly based lab.

```
.data
   x    BYTE    3
   y    BYTE    7
   z    BYTE    0
.code
   LOAD  A, [  x  ]
   LOAD  B, [  y  ]
   CMP   B, A
   BRGE  End
   STORE [  z  ], A
End:    NOOP
```